

Fig. 1

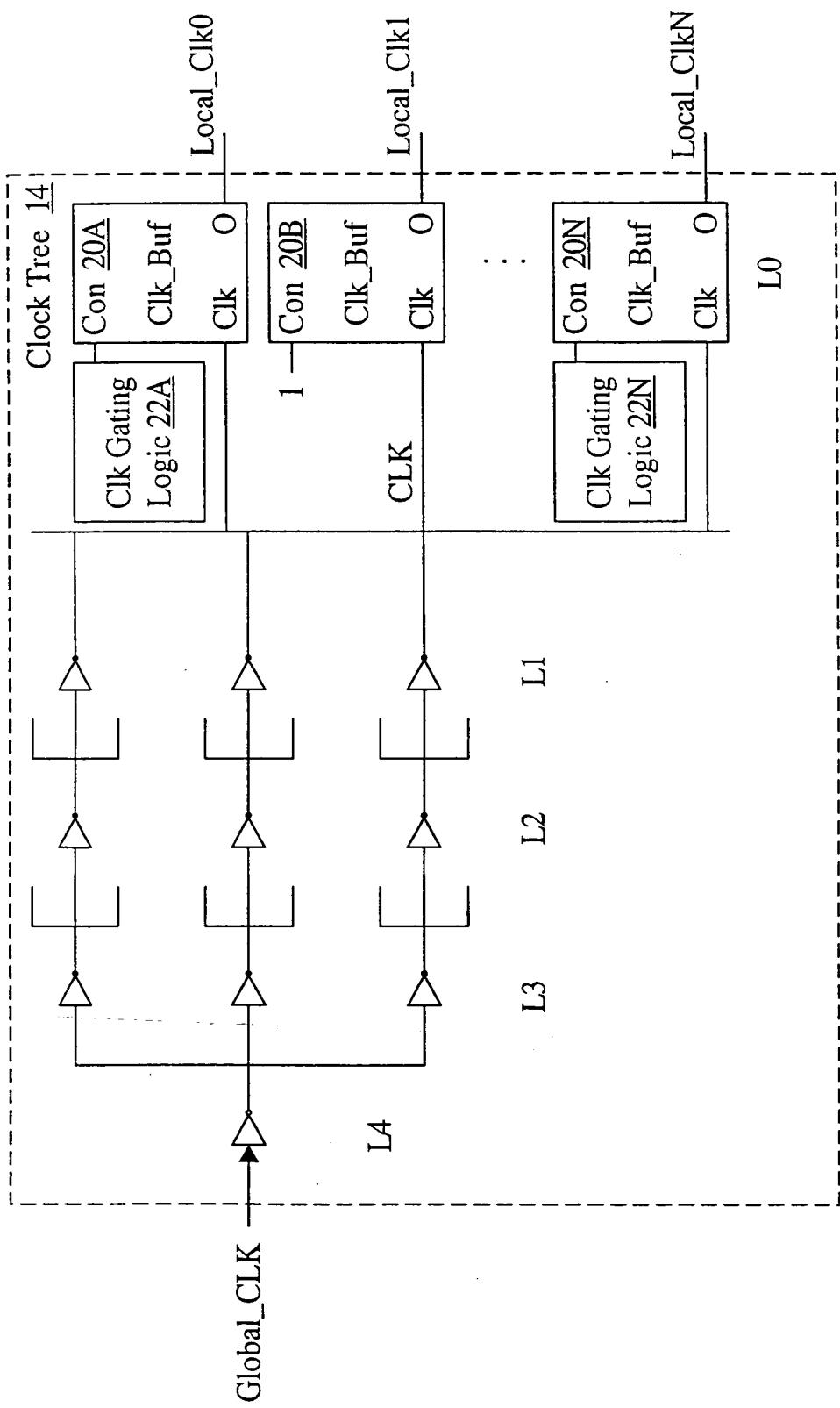


Fig. 2

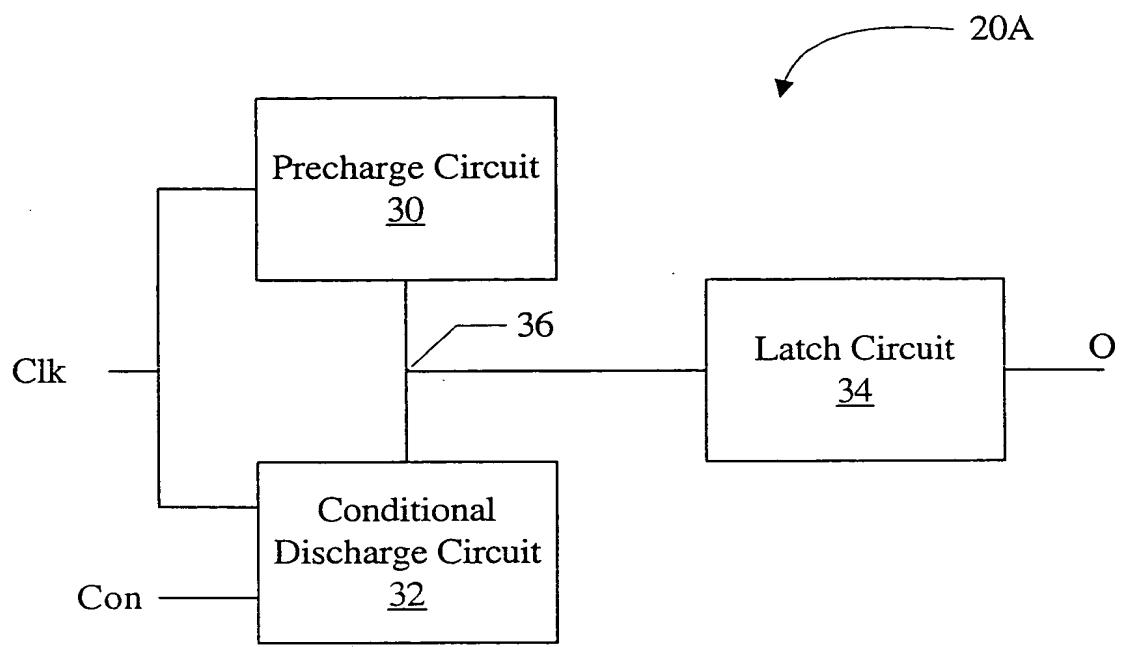


Fig. 3

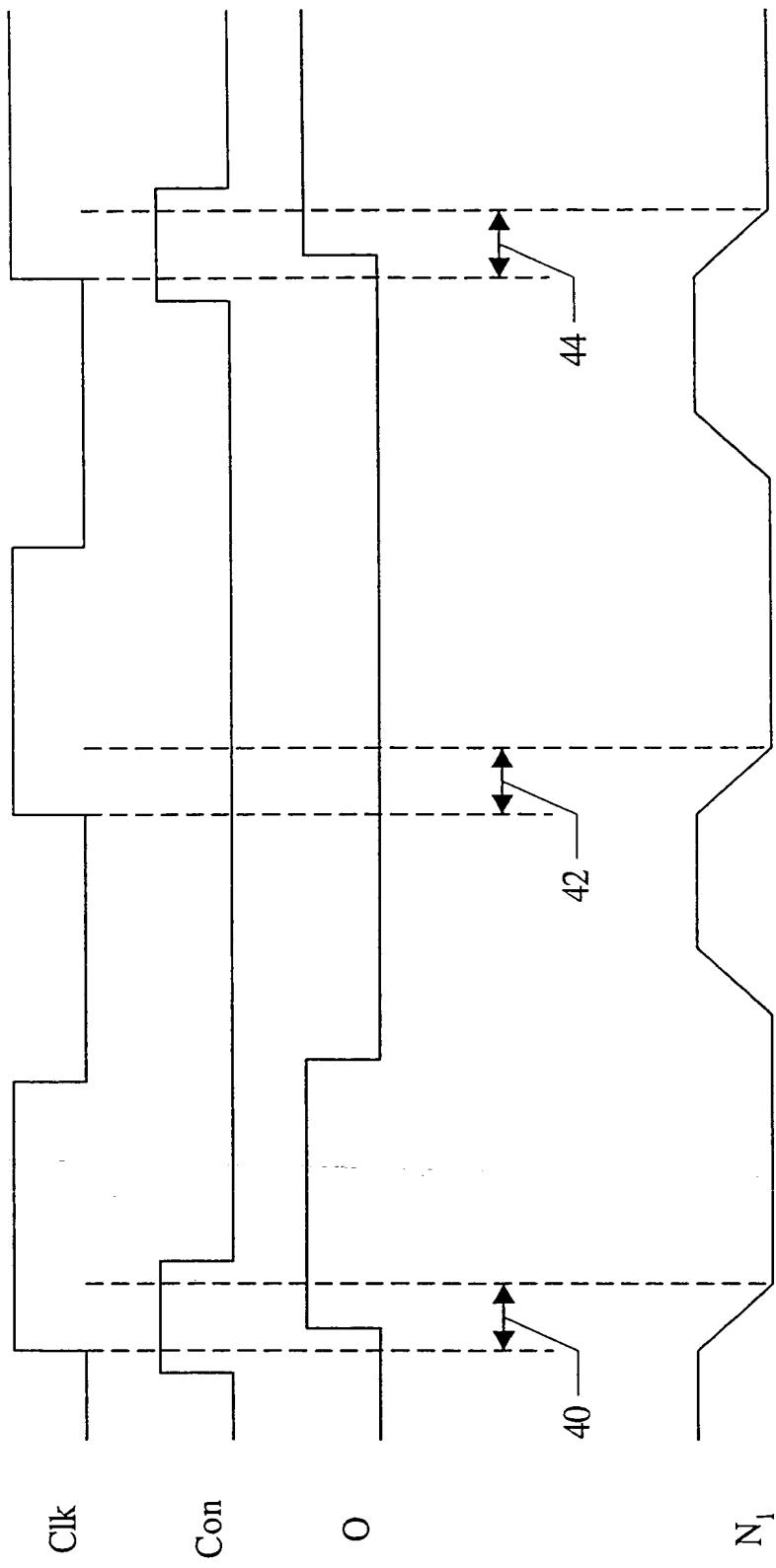


Fig. 4

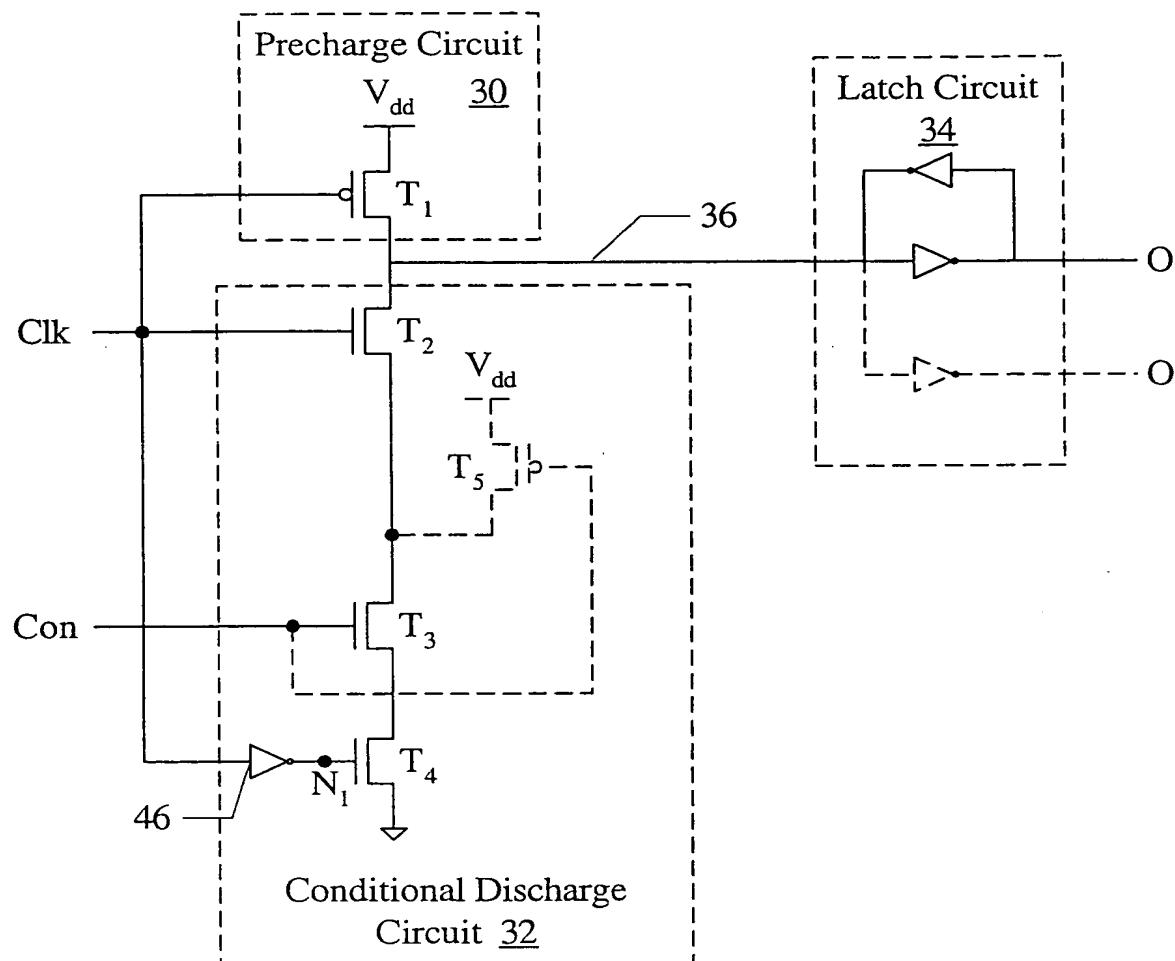


Fig. 5

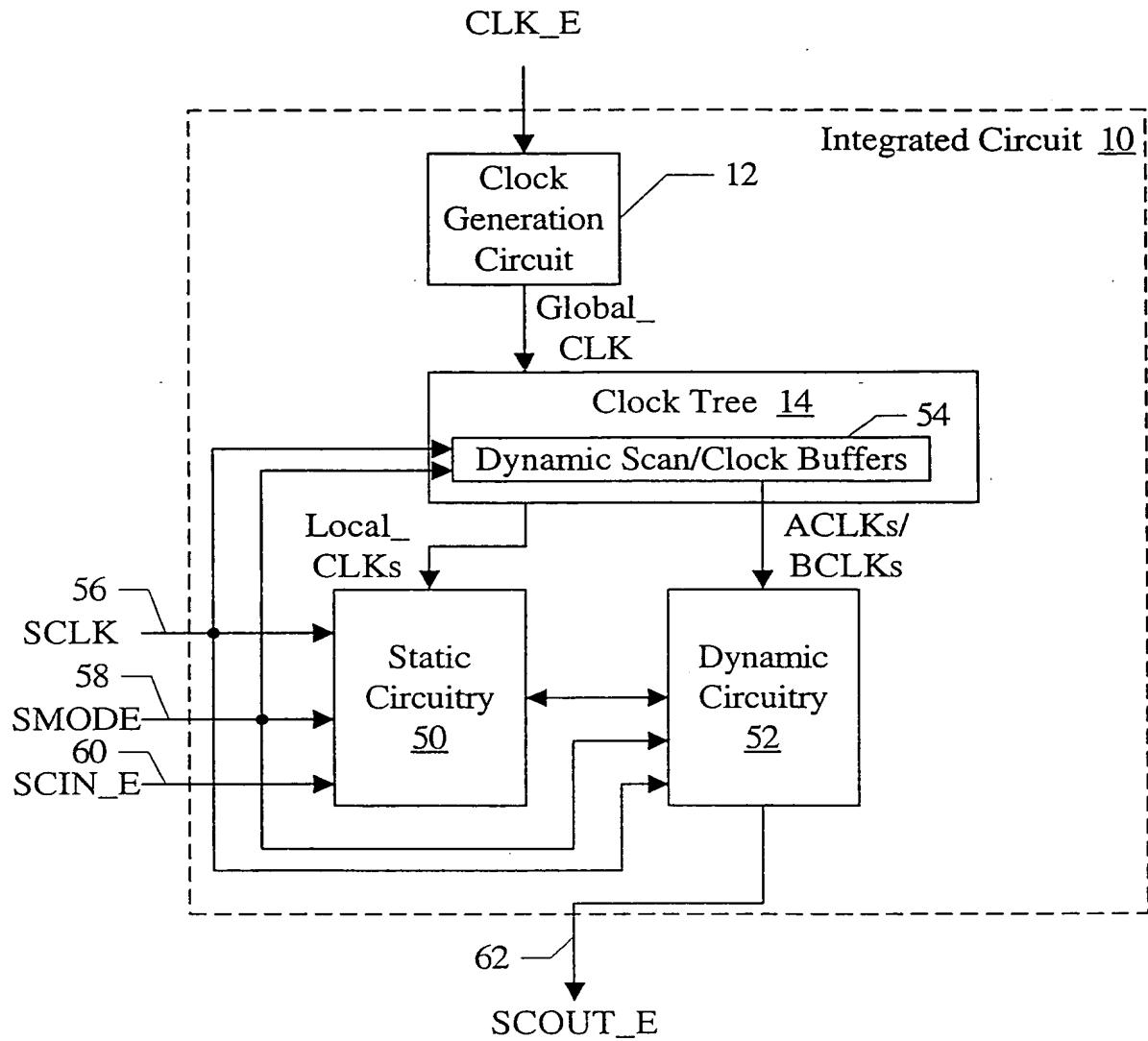


Fig. 6

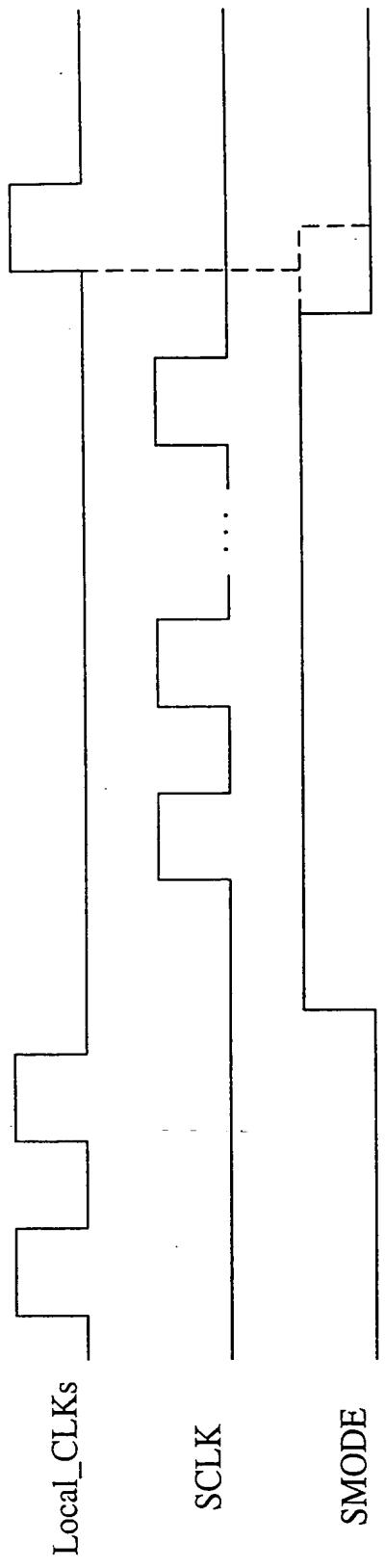


Fig. 7

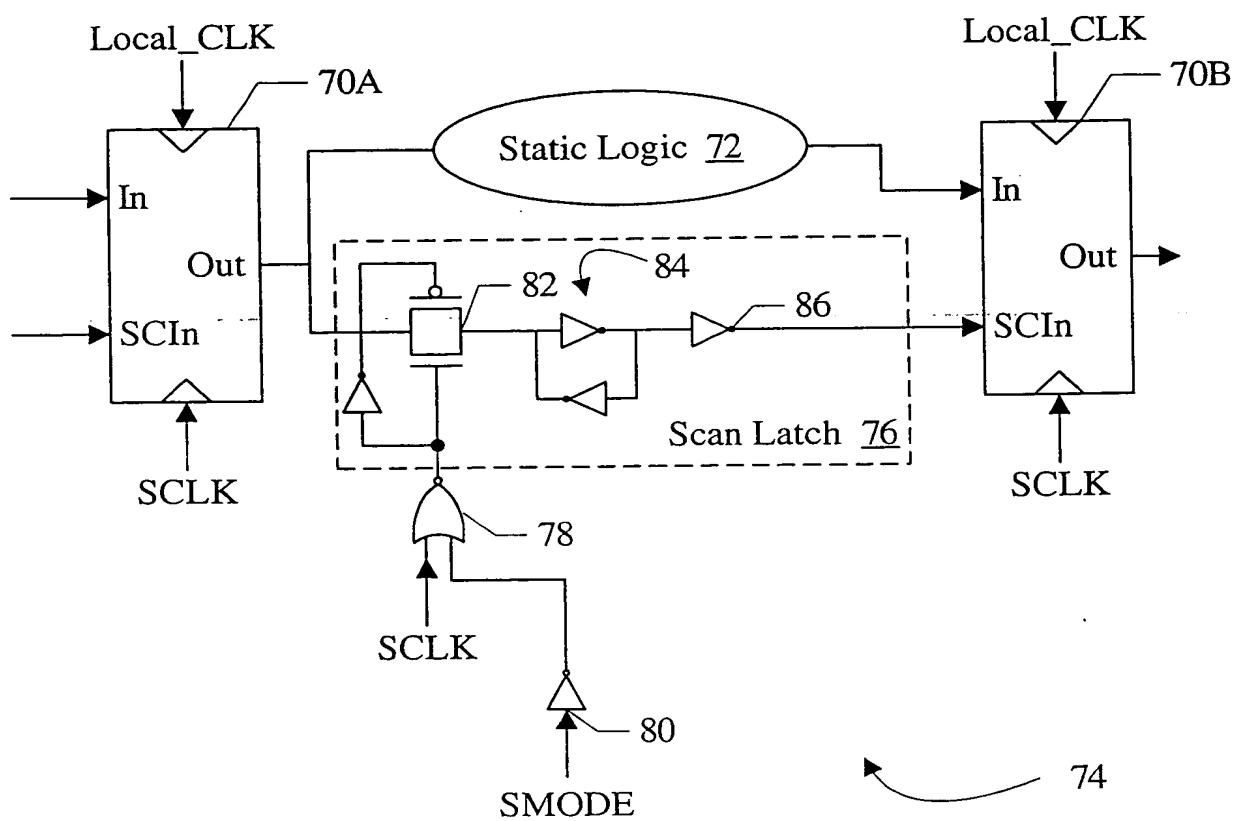


Fig. 8

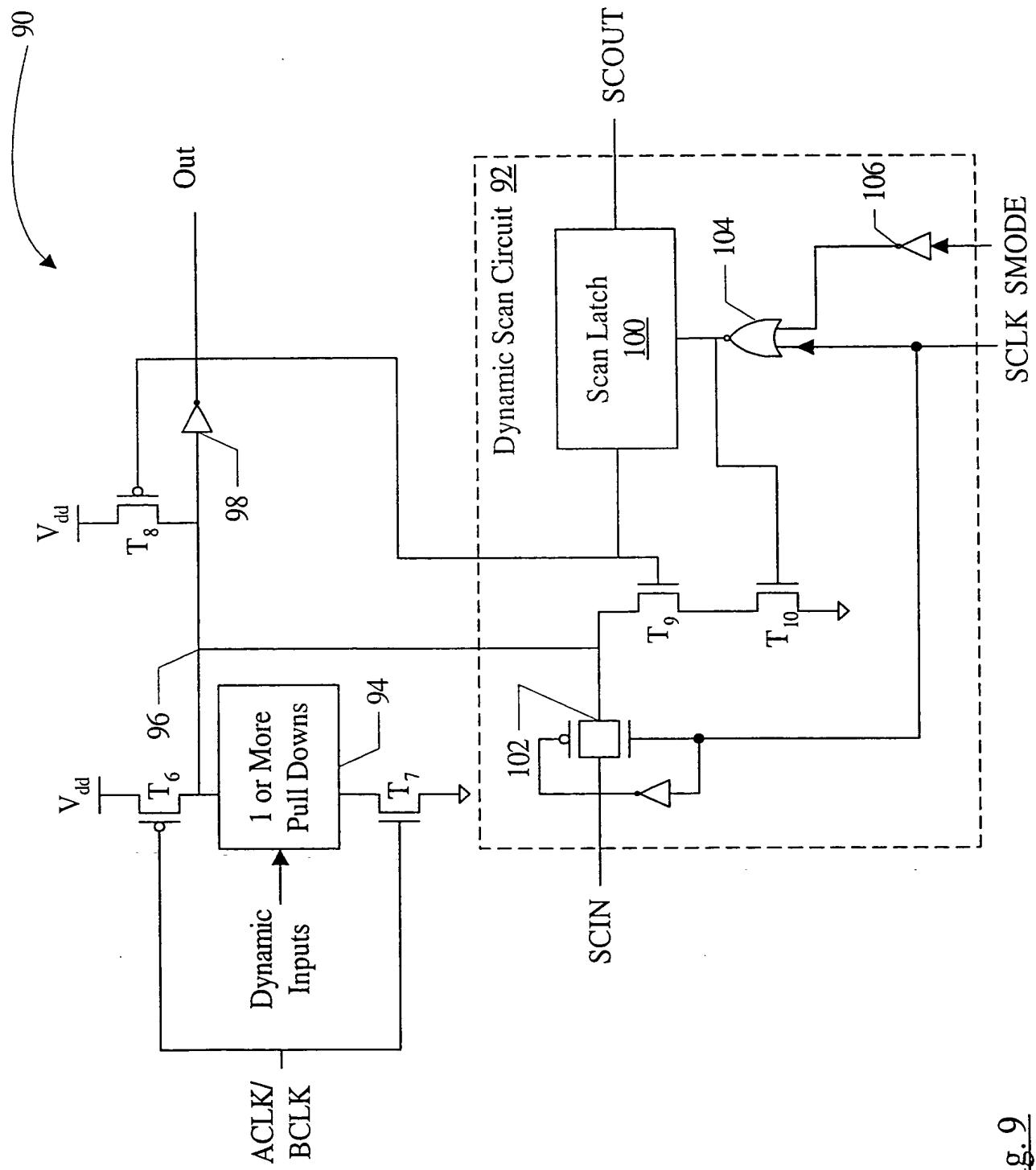
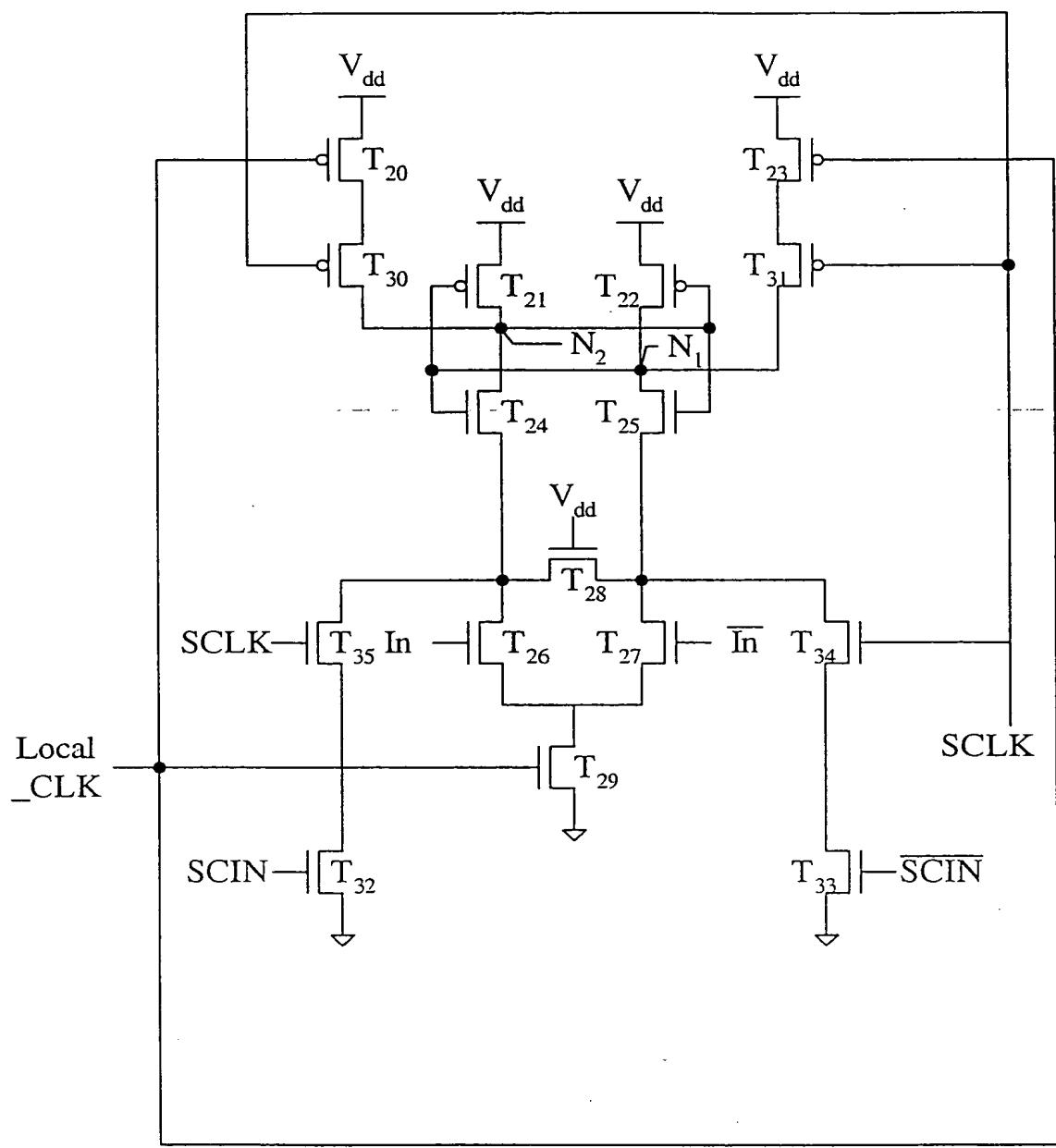


Fig. 9



110 ↗

Fig. 10

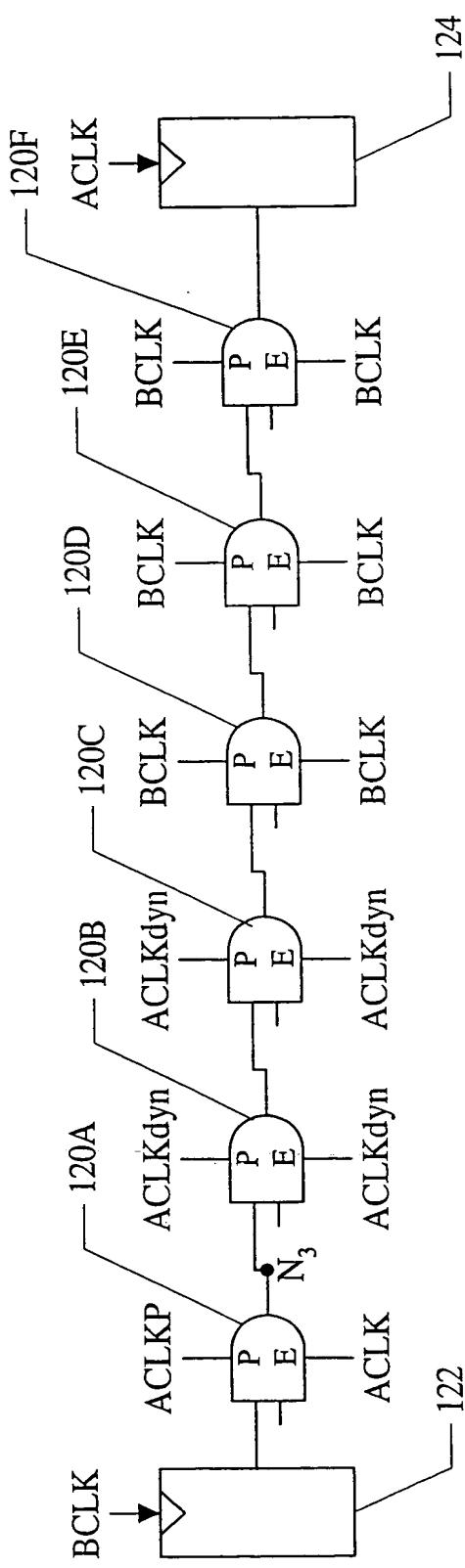


Fig. 11

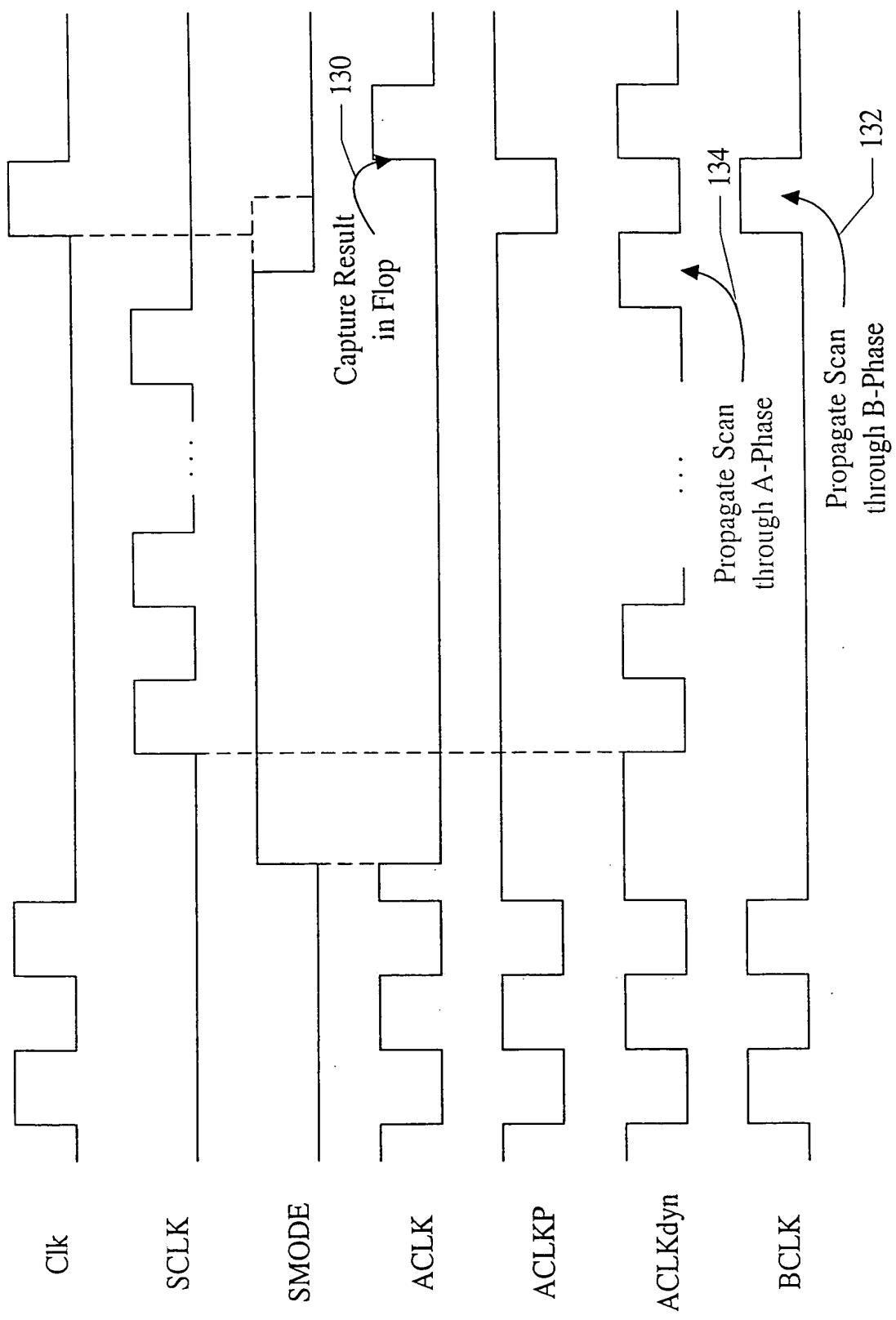
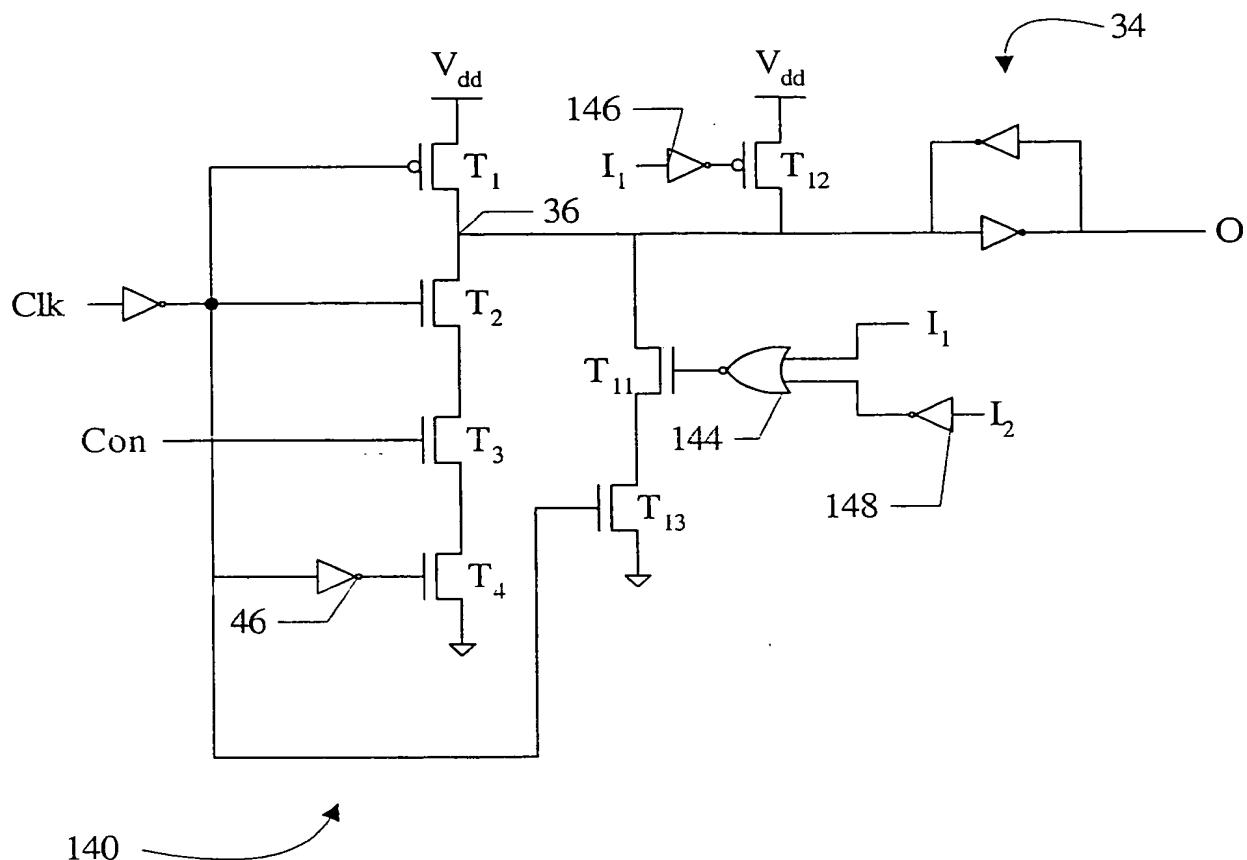


Fig. 12



O	ACLK	ACLKP	ACLKdyn
I ₁	S MODE	GND	SCLK
I ₂	GND	S MODE	S MODE

Fig. 13

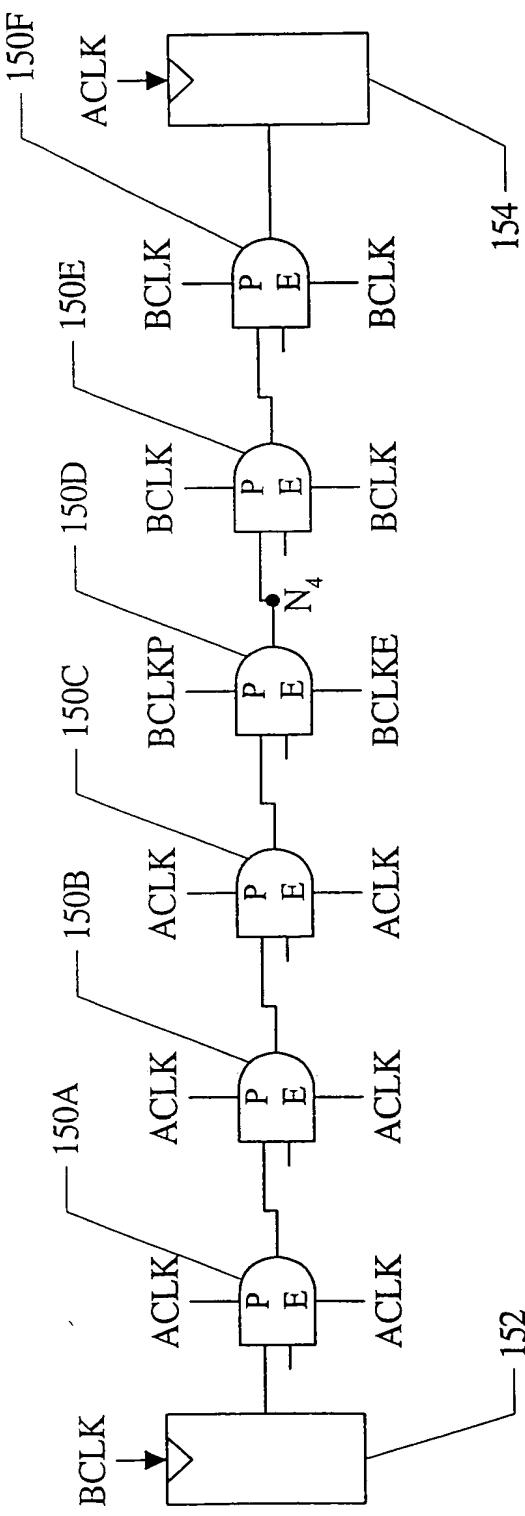


Fig. 14

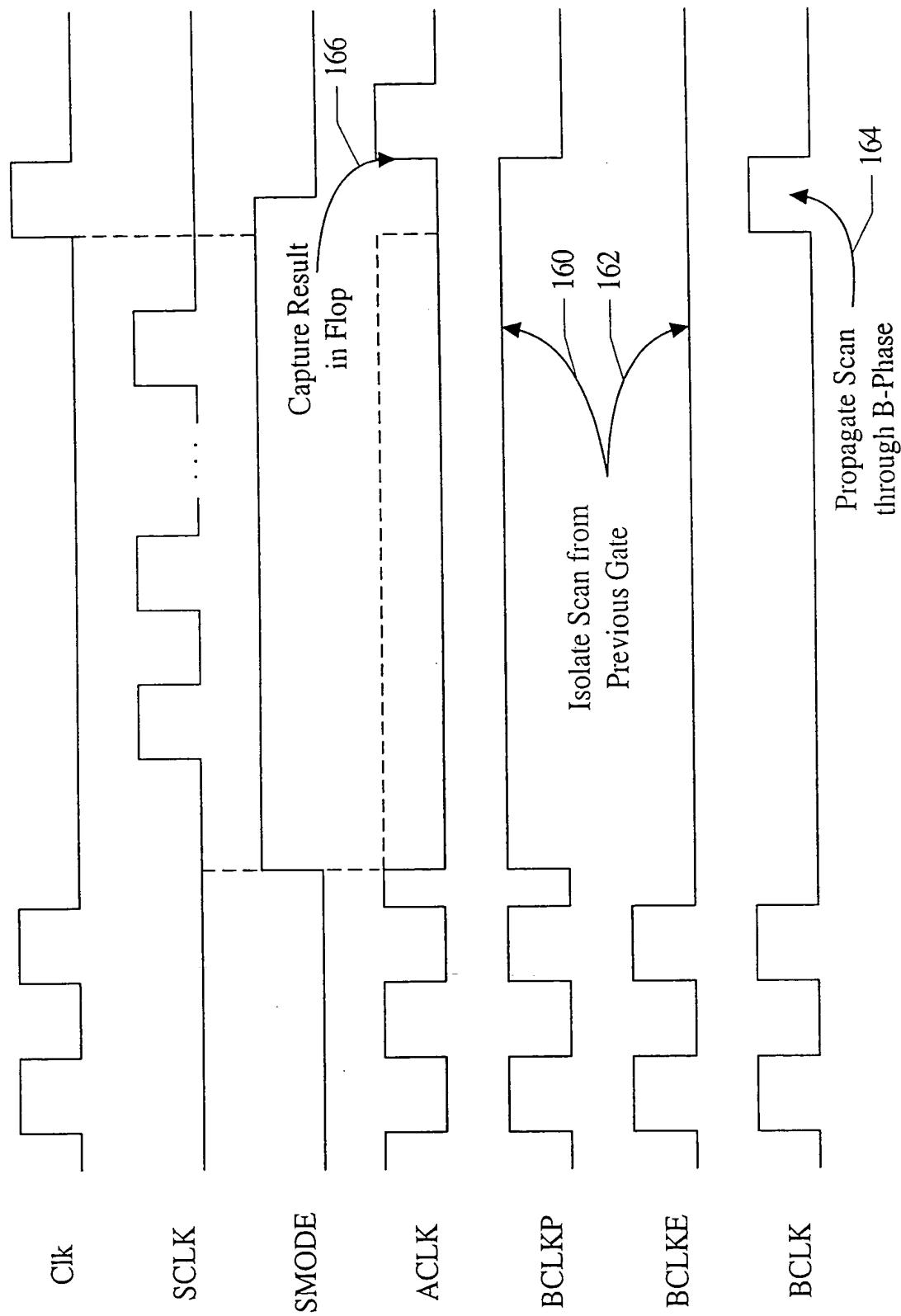
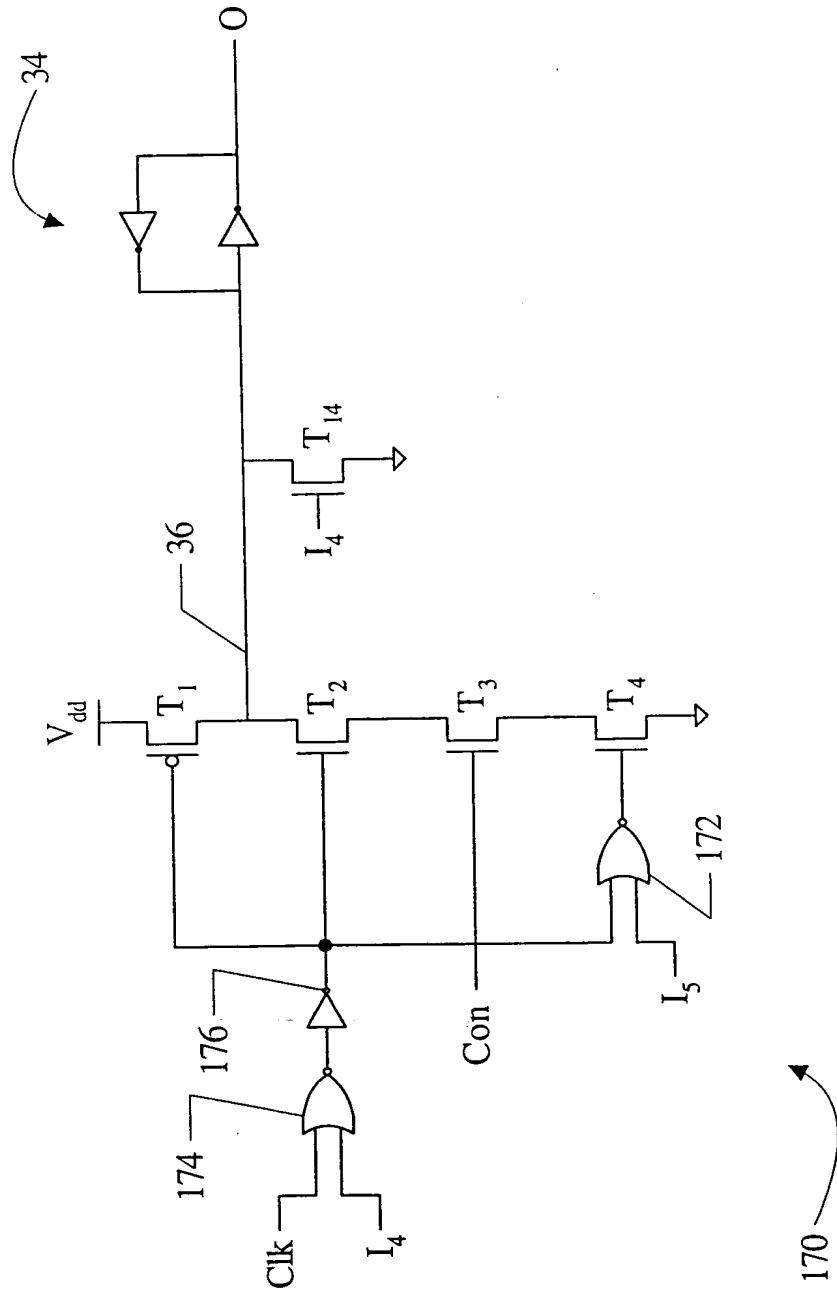


Fig. 15



	BCLK	BCLKE	BCLKP
I ₄	GND	GND	S MODE
I ₅	GND	S MODE	GND

Fig. 16

Computer Accessible Medium
300

Integrated Circuit
10

Fig. 17